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INFORMATION DISCLOSURE STATEMENT BY APPLICANT Date Submitted: May 6, 2009 <i>(use as many sheets as necessary)</i>		Application Number	10/567,832
		Filing Date	8/12/2004
		First Named Inventor	Karim S. KARIM
		Art Unit	2884
		Examiner Name	Mark R. GAWORECKI
Attorney Docket Number	069566-0400		
Sheet	1	of	2

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5,665,959	09-09-1997	Fossum et al.	

UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS					
Examiner Initials*	Cite No. ¹	U.S. Patent Application Document	Filing Date of Cited Document MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Serial Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 2000/019706	04-06-2000	Conexant Systems, Inc.		
		WO 2003/073507	09-04-2003	Planar Systems, Inc.		

Examiner Signature	/Mark Gaworecki/	Date Considered	05/21/2009
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		Attorney Docket Number	069566-0400

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
		Kavadias, et al., "A Logarithmic Response CMOS Image Sensor with On-Chip Calibration," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 35, No. 8, 2000.	
		Karim, et al., "On-Pixel Amorphous Silicon Amplifier for Digital Imaging in Integrated Optoelectronics," Deen, et al., editors; <i>The Electrochemical Society, Proc.</i> , Vol. PV2002-4, May 2002, pp. 313-332.	
		Karim, et al., "High Dynamic Range Pixel Architectures for Diagnostic Medical Imaging in Medical Imaging," <i>Physics of Medical Imaging, SPIE</i> , Vol. 5368, 2004, pp. 657-667.	
		Sanaie-Fard, et al., "High Dynamic Range Pixel Amplifier Architecture in Amorphous Silicon Technology for Diagnostic X-Ray Imaging Applications," <i>106th Meeting of the Electrochemical Society: Thin Film Transistor Technologies VIII (TFTTVIII)</i> , PV 2004-15, pp. 289-300.	
		Taghibakhsh, et al., "High Dynamic Range 2-FT Amplified Pixel Sensor Architecture for Digital Mammography Tomosynthesis," <i>IET Circuits Devices & Systems</i> , Vol. 1, No. 1, 2007, pp. 87-92.	
		Taghibakhsh, et al., "Amplified Pixel Sensor Architecture for Low Dose Computed Tomography Using Silicon Thin Film Technology," <i>Proceedings of SPIE</i> , Vol. 6510, 2007.	

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